

### **IN THE CLAIMS**

Please amend the claims as follows.

1. (Original) A method of forming a package, comprising:  
  
placing a film against a flip-chip assembly, wherein the flip-chip assembly includes a die, an electrical connection, and a mounting substrate;  
  
underfilling the die with underfill material;  
  
curing the underfill material; and  
  
after beginning curing the underfill material, removing the film.
2. (Original) The method according to claim 1, wherein the film includes a tacky film.
3. (Original) The method according to claim 1, wherein the film includes a tacky film, and wherein curing the underfill material is carried out under heat that causes the tacky film to release from the flip-chip assembly.
4. (Original) The method according to claim 1, wherein after beginning curing the underfill material and removing the film, curing includes:  
  
curing the underfill material that is in contact with the film;  
  
removing the film; and thereafter  
  
curing the underfill material that is between the die and the mounting

substrate.

5. (Original) The method according to claim 1, wherein after beginning curing the underfill material and removing the film, curing includes:

curing the underfill material that is in contact with the film by conductive heat transfer from a mold press;

removing the film; and thereafter

curing the underfill material that is between the die and the mounting substrate by placing the package into a curing oven.

6. (Original) The method according to claim 1, wherein after beginning curing the underfill material and removing the film, curing includes:

heating the package in a curing oven under conditions to cause the tacky film to release from the flip-chip assembly.

7. (Original) The method according to claim 1, wherein after beginning curing the underfill material and removing the film, curing includes:

heating the package in a curing oven under conditions to cause the tacky film to release from the flip-chip assembly, wherein heating includes a first temperature ramp to a temperature range from about 100° C to about 180° C, a temperature hold at a temperature in this range, a second temperature ramp to a temperature range from about 140° C to about 260° C, and cooling.

8. (Original) The method according to claim 1, wherein after beginning curing the underfill material and removing the film, curing includes:

heating the package in a curing oven under conditions to cause the tacky film to release from the flip-chip assembly, wherein heating includes a single step temperature ramp to a temperature in a range from about 1400° C to about 2400° C; and  
cooling.

Claims 9-16. (Canceled).

17. (Original) A chip package comprising:  
a die;  
a mounting substrate;  
an electrical connection disposed between the mounting substrate and the die;  
a cured underfill material including a fillet portion, and an interstitial portion disposed between the die and the mounting substrate, wherein the fillet portion includes a surface roughness and pattern that is characteristic of an interstitial film surface roughness and pattern.

18. (Original) The chip package according to claim 17, wherein the interstitial film surface roughness and pattern is derived from a film selected from a tacky

film and a non-tacky film.

19. (Original) The chip package according to claim 17, wherein the fillet portion exhibits a single-stage solidification profile in cross section.

20. (Original) The chip package according to claim 17, wherein the fillet portion exhibits a symmetrical rectilinear or other controllable footprint on the mounting substrate.

21. (Original) The chip package according to claim 17, wherein the fillet portion exhibits a concave curvilinear cross-sectional profile.

22. (Original) The chip package according to claim 17, wherein the electrical connection disposed between the mounting substrate and the die is selected from a ball grid array, a collapsed ball grid array, and a pin grid array.

23. (Original) A chip-packaging process system comprising:  
a die;  
a mounting substrate;  
an electrical connection disposed between the mounting substrate and the die;  
a tacky film that is disposed over the die and stretched onto the mounting

substrate;

a mold press that gives a shape to the film;

an underfill material disposed between the die and the mounting substrate;

and an underfill inlet and outlet system that communicates through the film.

24. (Original) The chip-packaging process system according to claim 23, wherein the underfill inlet and outlet system includes an underfill conduit and a vent.

25. (Original) The chip-packaging process system according to claim 23, wherein the underfill material includes a fillet shape disposed between the die and the mounting substrate, and wherein the a mold press that gives shape to the film includes a heater element disposed at the fillet.

26. (Original) The chip-packaging process system according to claim 23, further including:

a first heating source for ramping the temperature of the underfill material to a first cure state; and

second heating source for causing the tacky film to release from the die, the fillet, and the mounting substrate.